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Substitute for form 1449B/PTO			
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>			
(Use as many sheets as necessary)			
Sheet	1	of	2
Attorney Docket Number IB-1997			

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		WU, ET AL., "Germanium Nanowire Growth via Simple Vapor Transport," The American Chemical Society, Vol. 12 (No. 3), p. 605-607, (2000).	
		CUI, ET AL., "Diameter-Controlled Synthesis of Single-Crystal Silicon Nanowires," Applied Physics Letters, Vol. 78 (No. 15), p. 2214-2216, (April 9, 2001).	
		CUI, ET AL., "Doping and Electrical Transport in Silicon Nanowires," The Journal of Physical Chemistry B, Vol. 104 (No. 22), p. 5213-5216, (June 8, 2000).	
		WHANG, ET AL., "Large-Scale Hierarchical Organization of Nanowire Arrays for Integrated Nanosystems," Nano Letters, Vol. 3 (No. 9), p. 1255-1259, (2003).	
		HUANG, ET AL., "Directed Assembly of One-Dimensional Nanostructures into Functional Networks," Science Magazine, Vol. 291, p. 630-633, (January 26, 2001).	
		CUI, ET AL., "Functional Nanoscale Electronic Devices Assembled using Silicon Nanowire Building Blocks," Science Magazine, Vol. 291, p. 851-853, (February 2, 2001).	
		MELOSH, ET AL., "Ultrahigh-Density Nanowire Lattices and Circuits," Science Magazine, Vol. 300, p. 112-115, (April 4, 2003).	
		CHOI, ET AL., "Nanoscale CMOS Spacer FinFET for the Terabit Era," IEEE Electron Device Letters, Vol. 23 (No. 1), p. 25-27, (January 2002).	
		CHOI, ET AL., "Spacer FinFET: Nanoscale Double-Gate CMOS Technology for the Terabit Era," Solid-State Electronics, Vol. 46, p. 1596-1601, (2002).	
		CUI, ET AL., "Nanowire Nanosensors for Highly Sensitive and Selective Detection of Biological and Chemical Species," Science, Vol. 293, p. 1289-1292, (August 17, 2001).	

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 608. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Substitute for form 1449B/PTO		<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		<i>Application Number</i>	10/599,106
		<i>Filing Date</i>	09/19/2006
		<i>First Named Inventor</i>	Ji Zhu
		<i>Art Unit</i>	2812
		<i>Examiner Name</i>	Unknown
(Use as many sheets as necessary)			
Sheet	2	of	2
		<i>Attorney Docket Number</i>	IR-1997

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**NON-PATENT LITERATURE DOCUMENTS**

Examiner Signature	/Allan Olsen/	Date Considered	12/15/2010
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